a) INTR	b) TRAP	c) RST6.5		
	ne the 16 bit reginter b)	isters? Program counter	c) a & b	
	_	ardware interrupts?  b) INTR, T	TRAP	c) a & b
	vel Triggering in RAP	*	c):	RST7.5&RST6.5
5. Which stack a) FIFO	is used in 8085 b) Ll		c) FII	LO
a) Because	8085 processor	ed an 8 bit processor has 8 bit ALU. has 8 bit data bus.	?	
7. What is SIM	1?			

## Choose True or false from the following

1. Which interrupt has the highest priority?

- 8. HLT is a 1-byte instruction in which the address bus and the data bus are placed in high impedance state.
- 9. MOV instruction is a 1-byte instruction which copies data from source register to destination register.
- 10. Subroutine is a part of main program.